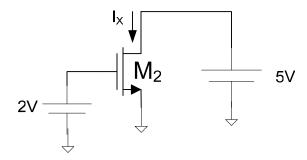
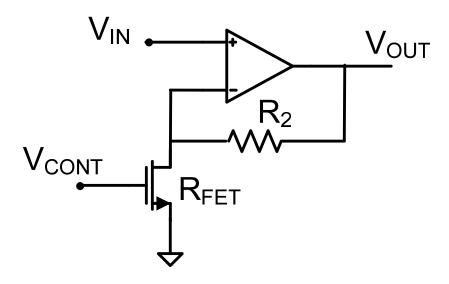
EE 230 Homework 10 Spring 2010

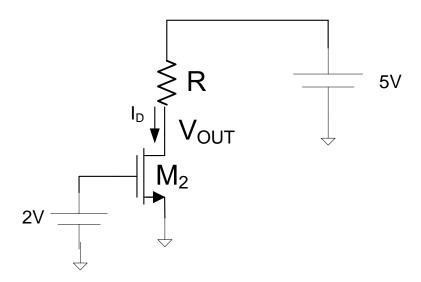
Problem 1 Assume the transistor M_2 shown below is characterized by the model on the last page of this HW assignment. Determine I_{X_1}



Problem 2 If the Op Amp is ideal and R₂=50K, determine the gain of the amplifier if V_{CONT} =4V. Assume the transistor has dimensions W=10u, L=6u. and that it is modeled by the parameters V_T =1V, uC_{OX} =1E-4 A/V², and λ =0V⁻¹.

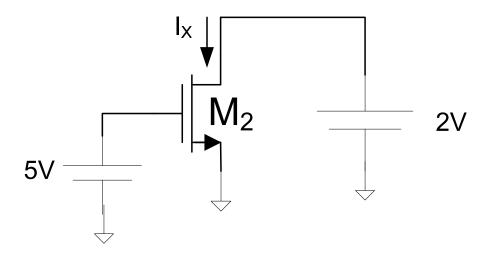


Problem 3 Determine V_{OUT} if the resistor R=5K and the transistor has dimensions W=10u, L=1u.. Assume the transistor is modeled by the parameters V_T =1V, uC_{OX} = 1E-4 A/V², and λ =0V⁻¹.

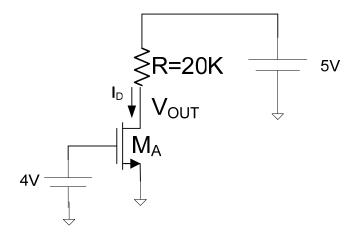


Problem 4 If a transistor has gate dimensions of W=45nm and L=45nm, how many transistors could be placed in an area equal to the cross-section of a 24ga copper wire? (neglect any spacing requirements between the transistors – this assumption introduces an error in the number of transistors that is somewhere around a factor of 10).

Problem 5 Size the transistor so that I_X =20mA. Assume the transistors is modeled by the parameters V_T =1V, uC_{OX} =1E-4 A/V^2 , and λ =0 V^{-1} .



Problem 6 Size the transistor so that V_{OUT} =1V. Assume the transistor is modeled by the parameters V_T =1V, uC_{OX} =1E-4 A/V², and λ =0V⁻¹.



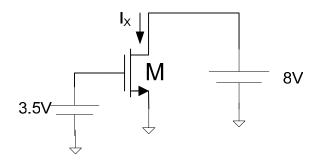
Problem 7 Assume the transistor is modeled by device designated as M_2 on the last page of this assignment. Obtain the simplified switch-level for this device shown below.

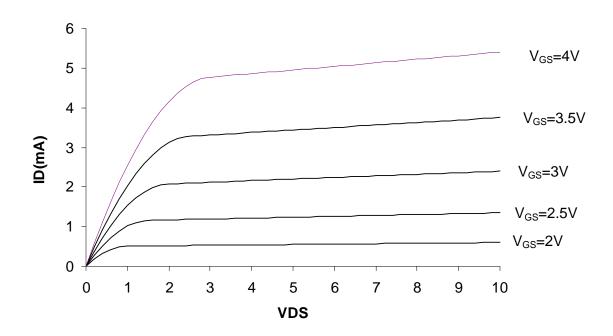


Problem 8 Give the circuit schematic of a 3-input NOR gate using n-channel and p-channel transistors. Assume the inputs are A, B, and C.

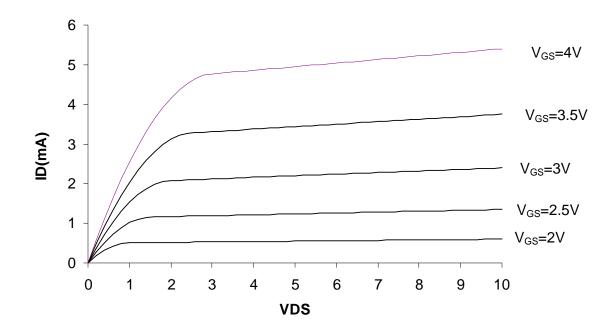
Problem 9 Assume the transistor M is characterized by the characteristics shown.

- Determine the current I_X. a)
- b)
- What region is the device operating in Repart parts a) and b) if the 8V source is reduced to 2V. c)

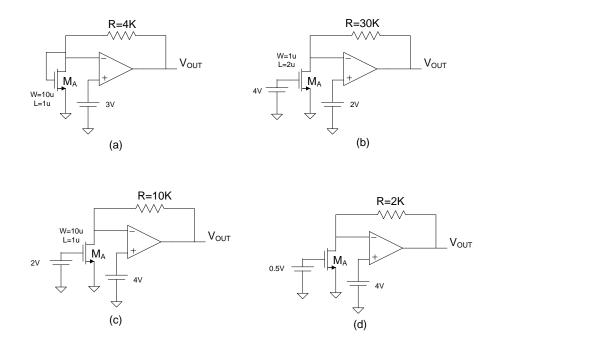




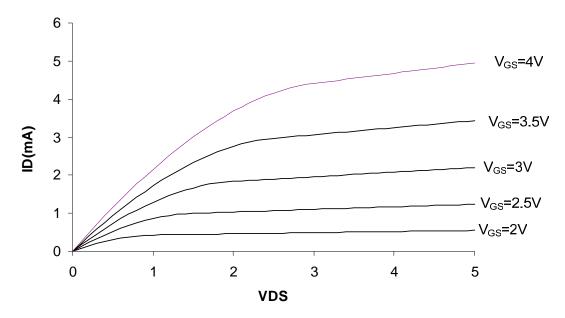
Problem 10 If an n-channel with dimensions W=20u and L=4u has the device characteristics shown below, determine the model parameters V_T and the product uC_{OX} .



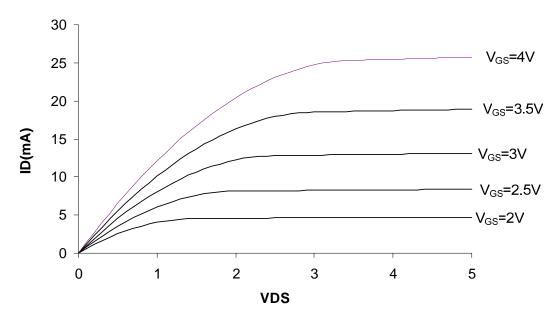
Problem 11 Determine V_{OUT} for the following circuits. Assume the transistors are modeled by the parameters V_T =1V, uC_{OX} =1E-4 A/V^2 , and λ =0 V^{-1} .



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Transistor M_1 W=4u, L=2u, V_T =1V, uC_{OX} = 4E-4 A/V², λ =.075V⁻¹



Transistor M_2 W=80u, L=2u, V_T =0.5V, uC_{OX} = 1E-4 A/ V^2 , λ =.01 V^{-1}